

Docket No.: 01 P 04581 US
App. No.: 09/809,457

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Original) A method of arbitrating access to a bus having a plurality of ports, said method comprising the steps of:
assigning each port a unique address which defines a unique priority value;
generating a repeating, variable length frame;
bidding for access to the bus during at least one predefined clock cycle of the frame;
granting access to the bidding port having the highest priority; and
placing the other bidding port addresses in a queue.
2. (Original) A method according to claim 1, wherein each port maintains a copy of the queue.
3. (Original) A method according to claim 1, wherein bidding is only permitted when the queue is empty.
4. (Original) A method according to claim 1, wherein at least one cycle of the frame is reserved for transmission of message length, at least one cycle is reserved for transmission of destination address, and at least one cycle is reserved for the port having the destination address to assert a busy signal on the data bus.
5. (Original) A method according to claim 4, further comprising the steps of:
the port having access to the data bus transmitting a message length during the message length cycle of the frame; and
the port having access to the data bus transmitting a destination address during the destination address cycle of the frame.

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6. (Original) A method according to claim 5, further comprising the steps of:
the port having the destination address asserting the busy signal during the busy
cycle
of the frame; and
the port attempting to transmit to the busy port repeating bidding until the
message is
sent.

7. (Previously Presented) A method for improving bus performance and
bandwidth utilization in a parallel bus LAN, said method comprising the steps of:
providing a parallel bus LAN having a plurality of data lines and a clock bus; and
avoiding data collisions by implementing, during a portion of a variable length
time
frame, a bidding arbitration scheme such that only one bus user can transmit on
the bus at one time, the bidding arbitration scheme assigning each port a unique
address which defines a unique priority value, wherein access is granted to a port
having the highest priority and wherein the other port addresses are placed in a queue,
each port maintaining a copy of the queue.

8. (Original) A method according to claim 7, further comprising the step of
increasing the number of data lines and/or the frequency of the clock bus to increase
the bandwidth of the LAN.

9. (Original) A method according to claim 7, further comprising the step of
avoiding glare by assigning a priority to each bus user and granting access to the bus to
the highest priority user when two or more users simultaneously bid for bus access.

10. (Previously Presented) A method according to claim 7, each port having a
configurable hardware interface.

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11. (Previously Presented) A method for improving bus performance and bandwidth utilization in a parallel bus LAN, said method comprising the steps of:
providing a parallel bus LAN having a plurality of data lines and a clock bus; and
providing a plurality of bus ports, each bus port having a configurable hardware interface to thereby enable the LAN to be adapted for use with differing hardware interfaces; and
avoiding data collisions by implementing, during a portion of a variable length time frame, a bidding arbitration scheme such that only one bus user can transmit on the bus at one time, the bidding arbitration scheme assigning each port a unique address which defines a unique priority value, wherein access is granted to a port having the highest priority and wherein the other port addresses are placed in a queue, each port maintaining a copy of the queue.

12. (Canceled)

13. (Canceled)

14. (Currently Amended) A method according to claim 11 [[12]], further comprising the step of avoiding glare by assigning a priority to each bus user and granting access to the bus to the highest priority user when two or more users simultaneously bid for bus access.

15. (Original) A method according to claim 11, further comprising the step of increasing the number of data lines and/or the frequency of the clock bus to increase the bandwidth of the LAN.